

### REMARKS

Claims 1-74 were pending in the present application. Claims 1-36 and 38-73 were allowed, and claims 37 and 74 were rejected under 35 U.S.C. §102(a) as being anticipated by the applicant's admission of the prior art. The applicant has amended claims 37 and 74 and added new claims 75-82. No new matter has been added. In view of the foregoing amendments and the following remarks, the applicant respectfully submits that all claims are in condition for allowance.

#### **1. Response to Rejections under Section 102**

Claims 37 and 74 were rejected as being anticipated by the applicant's admission of the prior art. The applicant respectfully traverses the rejection.

As amended, claim 37 recites a method in which information about physical pixel boundaries in an output device space is obtained. Adjacent tiles are provided that cover at least a region of a computer graphics illustration that includes overlapping objects without regard to the overlapping objects. Each tile includes only pixels of the output device space that are wholly within the tile. A clipping path corresponding to the boundary of each tile is supplied, and the objects surrounded by each clipping path are processed to produce vector output. The vector output is passed as a stream to an output device.

The discussion of prior art in the background section does not include the step of providing adjacent tiles, where each tile includes only pixels of an output device space that are wholly within the tile. On the contrary, the applicant states that prior-art tiles may share some pixels along the tile borders. [pg. 2, lines 7-9] Therefore, each prior-art tile may include pixels that are not wholly within the tile. In addition, the prior-art tiles in the left-hand side of FIG. 2, while adjacent, clearly include pixels of pixel grid 100 that are not wholly within each respective tile.

Furthermore, the discussion of prior art in the background section does not include the step of obtaining information about physical pixel boundaries in an output device space. Pixel grid 100 in FIG. 1 is shown to illustrate how objects 110, 115, and 120 are oriented relative to

the pixels of an output device. The representation of pixel grid 100 in FIG. 1 does not imply that a method or computer program product processing objects 110, 115, and 120 has information about the physical pixel boundaries in an output device space. For at least the foregoing reasons, claim 37 is allowable.

Claim 74 is a computer program product claim that includes limitations corresponding to those of amended claim 37 and is allowable for at least that reason.

## **2. New Claims**

New claims 75-78 depend directly from claim 37 and are allowable for at least that reason. New claims 79-82 depend directly from claim 74 and are allowable for at least that reason.

Support for claims 75 and 79 can be found at least on page 3, lines 14-17.

Support for claims 76 and 80 can be found at least on page 7, lines 25-27 and on page 8, lines 19-20.

Support for claims 77 and 81 can be found at least from page 7, line 14 to page 8, line 3.

Support for claims 78 and 82 can be found at least on page 7, lines 23-25.

## **3. Conclusion**

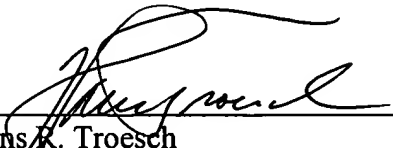
The applicant submits that all claims are in condition for allowance, and asks that all claims be allowed. Enclosed is a \$144 check for excess claim fees. Please apply any other charges or credits to deposit account 06-1050.

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Respectfully submitted,

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